

**Amendments to the claims are as follows:**

1. (Currently Amended) A multilayered circuit board, comprising:
  - at least first and second stacked insulating layers, wherein the first insulating layer has thereon a first electric conductor made of a conductive film constituting an inductor and a first electrode made of a conductive film constituting a first portion of a capacitor; and
  - the second insulating layer has thereon a second electrode made of a conductive film constituting a second portion of the capacitor; wherein the first and second insulating layers are stacked such that the first and second electrodes are opposed to each other through the insulating layers.
2. (Currently Amended) A multilayered circuit board according to Claim 1, wherein:
  - the first electric conductor is arranged along anthe outer periphery of the first insulating layer; and
  - the first electrode is arranged inside the first electric conductor and in athe center of the first insulating layer.
3. (Original) A multilayered circuit board according to Claim 2, wherein the first electric conductor and the first electrode are connected to each other with a first connecting conductor made of a conductive film.
4. (Currently Amended) A multilayered circuit board according to Claim 3, wherein:
  - the first electric conductor has a first extension; and
  - the second electrode has a second extension; wherein the first inductor and the capacitor are connected in series between the first and second extensions.

5. (Currently Amended) A multilayered circuit board according to  
Claim 3, wherein:

the second insulating layer comprises:

a second electric conductor formed of a conductive film  
constituting a secondan inductor along anthe outer periphery; and

the second electrode formed inside the second electric  
conductor and in atthe center of the second insulating layer; wherein

the second electric conductor has a first extension and  
the second electrode has a second extension; and

the inductors and the capacitor are connected in series  
between the first and second extensions.

6. (Currently Amended) A multilayered circuit board according to  
Claim 2, further comprising at least one third insulating layer stacked on the  
first and second insulating layers, wherein:

the third insulating layer has a third electric conductor formed of  
a conductive film constituting a firstan inductor;

the second insulating layer comprises:

a second electric conductor formed of a conductive film  
constituting a secondan inductor along anthe outer periphery; and

the second electrode is arranged inside the second  
electric conductor and in atthe center of the second insulating layer; and

wherein the first electric conductor has a first extension and the  
first electrode has a second extension; and

the inductors and the capacitor are connected in series between  
the first and second extensions.

7. (Currently Amended) A multilayered circuit board according to  
Claim 3, wherein:

the second insulating layer comprises:

a second electric conductor formed of a conductive film  
constituting an inductor along anthe outer periphery; and

the second electrode arranged inside the second electric conductor and in at the center of the second insulating layer; wherein the second electric conductor and the second electrode are connected to each other with a second connecting conductor formed of a conductive film;

the first connecting conductor has a first extension and the second connecting conductor has a second extension; and

the inductor and the capacitor are connected in parallel between the first and second extensions.

8. (Currently Amended) A multilayered circuit board according to Claim 3, further comprising at least one third insulating layer stacked on the first and second insulating layers, wherein:

the third insulating layer has a third electric conductor formed of a conductive film constituting a first inductor;

the second insulating layer comprises:

a second electric conductor formed of a conductive film constituting a second inductor along an the outer periphery; and

the second electrode arranged inside the second electric conductor and in at the center of the second insulating layer; wherein

the second electric conductor and the second electrode are connected to each other with a second connecting conductor formed of a conductive film;

the first connecting conductor has a first extension and the third electric conductor has a second extension; and

the inductors and the capacitor are connected in parallel between the first and second extensions.